

A PROOF SYSTEM FOR BRINCH HANSEN'S DISTRIBUTED PROCESSES

by

Marly Roncken
Niek van Diepen
Mark Kramer
Willem P. de Roever

RUU-CS-81-5

February 1981



Rijksuniversiteit Utrecht

Vakgroep informatica

Princetonplein 5
Postbus 80.002
3508 TA Utrecht
Telefoon 030-531454
The Netherlands

A PROOF SYSTEM FOR BRINCH HANSEN'S DISTRIBUTED PROCESSES

by

Marly Roncken

Niek van Diepen

Mark Kramer

Willem P. de Roever

Technical Report RUU-CS-81-5

February 1981

Department of Computer Science
University of Utrecht
P.O. Box 80.002, 3508 TA Utrecht
the Netherlands

A PROOF SYSTEM FOR BRINCH HANSEN'S DISTRIBUTED PROCESSES

by

Marly Roncken

Niek van Diepen

Mark Kramer

Willem P. de Roever

University of Utrecht

February 1981

Abstract. A proof system is presented for proving partial correctness of Brinch Hansen's Distributed Processes. The system is not complete for programs that may deadlock because of inter-process communication. Proofs of single processes are given in relation to their parallel environment. As usual, a slight mistake was found in a published concurrent algorithm. This time, it concerns a sorting algorithm that can be used as a priority scheduler. The adapted version (for maintaining a priority scheduler) is proved correct.

Keywords and Phrases:

distributed processes in general and Brinch Hansen's notion of them in particular, monitors, concurrency, (simultaneous) message passing, partial correctness in absence of global deadlock, auxiliary variables, process invariant, global invariant, (proof theoretical) environment, proof outline.

CR Categories: 5.24.

1. INTRODUCTION

We present a Hoare-like proof system for Distributed Processes (DP), a language concept defined by Brinch Hansen [4]. This system deals with proofs of partial correctness concerning programs that are free from deadlock caused by external requests (cf. section 7).

By a distributed model we mean a model, in which there is a system of processors, each having its own local store, and in which processors interact by means of message passing. For these models it seems more

natural to look upon synchronization as a simultaneous rather than mutual exclusive action. On this observation we base our proof system.

Because communication may alter the store of a process, and hence may affect the validity of assumptions made in a proof, we need a system that considers proofs of properties about a single process not only within the context of this process but within the context of the whole program. This implies the validity of assertions in a proof of this process with respect to the complete program.

Therefore, we introduce the (proof theoretical) environment of a program. This environment contains the text of the program together with some special assertions: a global invariant and for each process a process invariant. Introduction of this environment has been inspired by de Bakker's book [3].

The global invariant expresses the inter-process communications. It is used in a similar way as the global invariant in the CSP proof system [2].

Further guides for the system came from various related papers by: Apt, Francez and de Roever [1,2], Lamport [5], Levin [6], Owicki and Gries [8].

The rest of the paper is organized as follows. In section 2, we give a brief description of DP. Section 3 contains some simple examples to illustrate several aspects of the language. In section 4 the proof system is presented (4.1, 4.2), together with a discussion of the various rules (4.3). In section 5 the system is applied to prove the examples in section 3. Section 6 contains a more advanced example: a sorting program which is also applicable as a priority scheduler is proved correct. This example was taken from Brinch Hansen's paper [4], but has been adapted, because there was a slight mistake in the original version, which caused the program to function improperly as priority scheduler. Finally, section 7 summarizes and evaluates the results.

2. THE LANGUAGE CONCEPT DP

DP is especially conceived for real-time applications, controlled by microcomputer networks with distributed storage. A real-time program interacts with an environment in which many actions take place simultaneously at high speed. Therefore, it may be convenient that the nondeterministic requests to such programs from its environment are handled in arbitrary order, and that the program never terminates but continues to serve its

environment as long as the system works. The last two properties are clearly visible in DP's convention for process execution (cf. 2.4).

2.1. A DP program consists of a fixed number of concurrent and persistent processes that are started simultaneously. A process does not contain parallel statements.

The syntax of a process is as follows:

```
process<name>;
  <private variables>
  <common procedures>
  <initial statement>
```

2.2. Communication

A process can access its private (i.e., local) variables only. No common (i.e., shared) variables are used. However, a process can call common procedures within other processes (but not itself) in the program: external requests (short: requests). When such a request is honoured, a new incarnation of the common procedure being called is created for the requesting process. Syntax of a procedure:

```
proc<name>(<input parameters> # <output parameters>)
  <local variables>
  <statement>
```

('#' separates the formal input and output parameters; if only input parameters are used, the separator '#' is deleted.) An external request in process P to procedure qr within process Q is written as:

call Q.qr($u_1, \dots, u_n, v_1, \dots, v_m$), where the actual parameters u_1, \dots, u_n , respectively, v_1, \dots, v_m agree in number and type with the formal input, respectively, output parameters of procedure qr in process Q.

DP uses call-by-value-result for parameter transfers. To avoid unnecessary complications in the proof system, subscripted variables do not occur as parameters (ch. 9 of [3] shows how this complication can be solved, in general). Furthermore, an external request is regarded as an elementary action within the requesting process: in the above setting, process P is suspended until process Q has completed the request. In other words the possibility for implementing distributed recursion within a fixed network of processors, has not been realized. If inter-process communications had been allowed to the requesting process during

the call (i.e., after sending the values of the input variables, and before receiving the values of the output variables), then the full expressive power of recursion could have been exploited!

This possibility is too exciting to be ignored, and research is going on to find the appropriate version of Scott's induction rule in this new setting.

2.3. Synchronization in DP

Synchronization is established by means of nondeterministic statements: guarded regions. These statements have the following syntax and meaning:

('|' separates the guarded statements)

when-statement:

$$\underline{\text{when}} \ b_1 : S_1 \ | \ \dots \ | \ b_n : S_n \ \underline{\text{end}}$$

meaning:

wait until at least one of the conditions b_i is true, then select one of these arbitrarily and execute the corresponding statement S_i .

cycle-statement:

$$\underline{\text{cycle}} \ b_1 : S_1 \ | \ \dots \ | \ b_n : S_n \ \underline{\text{end}}$$

meaning:

endless repetition of the corresponding when-statement.

2.4. Process execution

A process begins by executing its initial statement. This continues until the statement either terminates or waits in front of a guarded region for a condition to become true. Then the process is idle until another operation is started as the result of an external request. Note that this is the only possible way to continue, for only the process itself can access the variables, occurring in the conditions of a guarded region within its initial statement.

When this operation terminates or waits in front of a guarded region, the process will

- (1) either begin yet another operation, i.e. honour an external request,
- (2) or resume an earlier operation as result of a condition becoming true,
- (3) or remain idle until one of the above situations occurs.

If the initial statement terminates, the process will still honour external requests. Globally, a process acts like a monitor but for implicit signalling operations, associated with guarded regions.

Brinch Hansen's processes never terminate. Hence Hoare's $\{p\} Q \{r\}$ formalism could be trivially applied with $r \equiv \underline{\text{false}}$. However, one can very well consider a program as being 'terminated' iff each of its processes

either (1) has terminated its initial statement
 or (2) has not yet terminated its initial statement, and control is
 at a guarded region within the initial statement with all
 (constituent) guards false.

Now $\{p\} Q \{r\}$ is valid iff for initial states satisfying p , upon 'termination' of Q , in the sense above, r holds for the possible resulting states.

Notice that if a process satisfies condition (1), it may very well be the case that some of its common procedures are being executed. Thus the question arises: if all processes satisfy condition (1) or (2), it is not possible that those processes which satisfy (2) are executing common procedures? The answer is no. A common procedure of one process being executed presupposes a call of that common procedure in another process, again not satisfying condition (2), which may occur in its turn in a common procedure body, etc.. The resulting chain of incarnations has a first call which necessarily occurs inside an initial statement neither satisfying condition (1) or (2). Hence the program is not 'terminated'.

We are working on formalizing this convention in a framework such as that of Lamport [5], in which properties of the location counter are expressible.

2.5. Nested external requests and recursive procedures

Program executions in which a suspended process (cf. 2.2) has to honour requests for computation to continue, are not considered correct executions. In particular recursive procedures are taboo in the DP language concept. Beside the ambiguity about this in Brinch Hansen's paper [4], the main reason for this convention is the following. While a process is suspended, as result of an external request within it, no operations take place in it. Consequently, this process is not able to honour a request; especially requests to its own procedures; consult section 2.2 for a possible extension of DP in which such requests can be honoured.

2.6. Nondeterminism

Nondeterminism in DP is introduced:

- (1) implicitly by the unpredictable order in which earlier operations are resumed, and
- (2) explicitly by means of guarded statements:
 - (i) guarded region : also enables synchronization (cf. 2.3)
 - (ii) guarded commands: with syntax and meaning as follows:
(as in 2.3, '|' separates the guarded statements)

if-statement:

$$\underline{\text{if}} \ b_1 : S_1 \ | \ \dots \ | \ b_n : S_n \ \underline{\text{end}}$$

meaning:

if some of the conditions are true, select one of them arbitrarily and execute the corresponding statement, otherwise abort the process.

do-statement:

$$\underline{\text{do}} \ b_1 : S_1 \ | \ \dots \ | \ b_n : S_n \ \underline{\text{end}}$$

meaning:

while some of the b_i 's are true, select one of them arbitrarily and execute the corresponding statement.

Note that in contrast with a guarded region, a guarded command cannot delay an operation.

2.7. A few notational remarks

- (1) There is a shorthand notation for an array of n identical processes within a program:
process name[n].
A standard function 'this' defines the identity of an individual process within this process-array. Likewise functions as 'succ' and 'pred' for the successor and the predecessor of an individual process, if any, may be added.
- (2) seq[n]T or array[n]T denote a sequence or array with at most n elements of type T.
- (3) We use $[P_1 \ || \ \dots \ || \ P_n]$ to denote the parallel execution of processes P_1, \dots, P_n ($n \geq 2$), i.e., execution of the DP program that consists of the processes P_1, \dots, P_n ($n \geq 2$).
- (4) Concatenation of array (or sequence) a_1 with array (or sequence) a_2 is denoted as: $a_1^{\wedge}a_2$. (This is needed in the proof system).

Further details on the language concept DP may be found in [4].

3. SOME SIMPLE EXAMPLES

- to illustrate several language aspects -

3.1. One of the simplest examples of message passing in DP programs is the following.

```
process P1;  
x : int  
begin x := 0; call P2.pass(x) end
```

```
process P2;  
y : int  
proc pass (z : int) begin y := z end  
begin y := 1 end
```

evidently, $y = 0$ is valid after termination of this program.

3.2. The next example illustrates the arbitrary order in which different outstanding requests to the same process are honoured.

Process 'soldier' with private variable 'salary' is requested simultaneously by an array of n processes (cf. 2.7). Honouring a request results in assigning the value the identity of the requesting process to 'salary'.

```
process soldier;  
salary:int  
proc change (z:int) begin salary := z end  
begin salary := 0 end  
  
process military_department[ n ] ;  
begin call soldier.change(this) end
```

After termination of this program the assertion:
 $\text{salary}=1 \vee \dots \vee \text{salary}=n$, will be valid. And this is indeed the strongest assertion possible !

3.3. Next, let us consider a program with synchronization by means of a when-statement. The single guard of this statement is initially made false and can only become true inside a procedure body in the same process. Consequently a request to the procedure is honoured before the process continues the execution of this when-statement.

Who believes that monkeys are not able to write nonsense in cooperation - even people can. In this program two monkeys are obliged to write "nonsense" for getting a banana. They can do this by typing the syllable 'HUM' or 'BUG', which in cooperation and depending on the typed order, may give 'HUMBUG'.

```

definition : mode syllable = seq[3]char.

process monkey[2];
proc eat(x) begin "eat x" end
begin call writer.type end

process writer;
b, c, d : bool; word : seq[2]syllable; message : seq[16]char
proc type if true : word := word^<HUM>; b := true; c := false;
    d := true
    | true : word := word^<BUG>;
    if c : d := false; b := true
    |  $\neg$ c : c := true
    end
    end
begin b := c := d := false; message := word := <>;
    when b : if c^d : call monkey[1].eat(banana);
    call monkey[2].eat(banana)
    |  $\neg$ (c^d) : message := <no_bananas_today>
    end
    end
end

```

Note: The boolean guard d is introduced to ensure termination of the program (b := true) without the possibility of incorrect feeding (d := false).

Unfortunately the writer of this program has fallen into monkeyism. For even though the monkeys succeed in typing 'HUMBUG', they cannot be sure of getting their desired bananas: process writer may resume the when-statement directly after the first honoured request is executed (that resulted in: word = <HUM>^b = true^c = false), and in that way stop the supply of bananas for today.

However, when the program terminates the assertion
word = <HUMBUG> \vee message = <no_bananas_today>, holds.

3.4. This example concerns the nesting of procedure calls.

A process-array calculates $n!$ when the input item is n , provided n is smaller than the length of this array. Item n is input from the user process through calculate-process [1], that outputs 1 if $n=0$, otherwise $n-1$ is passed to the successor, calculate-process [2]. The latter outputs 1 to its predecessor if $n-1=0$ and in the other case $n-2$ is passed to its successor, and so on.

Finally calculate-process [n+1] receives the value 0 and consequently outputs 1 to the user process if $n=0$, else to calculate-process[n]. In the latter case the remaining requests are finished successively by assigning the value of $(n+1 - \text{this}) * (\text{passed value})$ to one's predecessor in the sequence of nested requests. Remember that the parameter passing mechanism is call-by-value-result.

```

Process calculate [m] ;
proc faculty (x:int# y:int) if x>0 : call calculate[succ].faculty(x-1,y);
                                y := y*x
                                | x=0 : y := 1
                                end
begin skip end

process user;
value:int
begin call calculate [1] .faculty(value,value) end

```

3.5. For those familiar with CSP (Communicating Sequential Processes), a language concept by Hoare and related to DP [2,6,10], the former programs are expressed by:

- (1) $P_1 :: x := 0; P_2!x$
 $P_2 :: y := 1; P_1?y$
- (2) soldier $\equiv P_0 :: \text{salary} := 1; y := 1; * [P_1?y \rightarrow \text{salary} := y \square \dots$
 $\dots \square P_n?y \rightarrow \text{salary} := y]$
 $P_i :: z := i; P_0!z \quad , 1 \leq i \leq n.$

(3) $\text{monkey}[i] ::= [\underline{\text{true}} \rightarrow \text{writer!}\langle\text{HUM}\rangle \ \square \ \underline{\text{true}} \rightarrow \text{writer!}\langle\text{BUG}\rangle];$
 $\quad \quad \quad *[\text{writer?}y \rightarrow \text{"eat } y"] \quad \quad \quad (i = 1,2)$

$\text{writer} ::= \text{word} := \langle \rangle;$
 $\quad \quad \quad [\text{monkey}[1]?x \rightarrow \text{word} := \text{word}^x; \text{monkey}[2]?x; \text{word} := \text{word}^x$
 $\quad \quad \quad \square \ \text{monkey}[2]?x \rightarrow \text{word} := \text{word}^x; \text{monkey}[1]?x; \text{word} := \text{word}^x];$
 $\quad \quad \quad [\text{word} = \langle\text{HUMBUG}\rangle \rightarrow \text{monkey}[1]!\text{banana}; \text{monkey}[2]!\text{banana}$
 $\quad \quad \quad \square \ \text{word} = \langle\text{HUMBUG}\rangle \rightarrow \text{message} := \langle\text{no_bananas_today}\rangle]$

Note that in this program process writer is indeed a fair judge.

(4) $\text{user}_{\text{DEF}} P_0 ::= \text{value} := n; P_1!\text{value}; P_1?\text{value}$
 $P_i ::= [P_{i-1}?x \rightarrow [x > 0 \rightarrow P_{i+1}!(x-1); P_{i+1}?y; P_{i-1}!(x*y)$
 $\quad \quad \quad \square \ x = 0 \rightarrow P_{i-1}!1$
 $\quad \quad \quad]$
 $\quad \quad \quad],$
 $(1 \leq i \leq n).$

4. THE PROOF SYSTEM

4.1. Introduction of process invariant, global invariant, auxiliary variables, bracketed sections and environment.

The absence of global variables in DP programs creates an ideal situation to prove properties about programs by proving properties of the processes within them in isolation, and then deducing the desired properties by comparison. Assertions in a separate proof of a process may contain only private variables and private auxiliary variables of this process, analogous to Owicki's system [7] and the CSP proof system [2]. An inevitable phenomenon of languages for parallel programs, however, is that the meaning of a single process is no longer independent of its context, in contrast with sequential programs in which, for instance, the meaning of a procedure (without global variables) can be defined in situ. Consequently, a proof system that captures deduction of properties about a program, but that contains no meta elements such as the cooperation test in the proofsystem for CSP or Owicki's interference freedom test, must provide these elements (i.e., context dependency of isolated proofs) implicitly. Obviously, this

context dependency is expressed by predicates on values received at synchronization which took place when the process in question was waiting. This leads to the following definition.

definition 1: A process is at a waiting point (cf. 2.4):

- (1) before a guarded region when all its guards evaluate to false,
- (2) on termination of the initial statement,
- (3) on completion of an honoured request.

For a proof system, this implies that rules concerning waiting points must provide assertions about the actual state of the process. Since we disregard programs that may deadlock because of inter-process communication, the assertions in a proof of a process do not provide information about suspension of the process, but are concerned only with the values of the private variables and the parameters of the process. Consequently, the actual state of the process at a waiting point can be expressed by means of relations between, and values of, these private variables; this will be done in the, so called, process invariant.

Before we continue with the process invariant, first some remarks about auxiliary variables. The need for these variables is a familiar fact in proofs concerning parallel programs.

definition 2: AV = set of auxiliary variables, different from program variables and parameter incarnations;

Variables in AV do not affect the flow of control in a program and appear only in assignments of the form $x := t$, where $x \in AV$.

Each process has its own set of auxiliary variables.

A process invariant, denoted $PI_{\langle \text{process name} \rangle}$ or, if the processes are numbered, $PI_{\langle \text{process index} \rangle}$, may contain only private variables and private auxiliary variables.

Henceforth, in references to the process invariant in general, the invariant will be denoted by PI.

PI must hold at all waiting points in the process corresponding with PI and, as such, provide information about the actual state of this process (notice the analogy with monitor invariants).

However, it is not enough to add the PI's to the proof system. Consider the following example:

```

process P[2];
j[this] : int
begin j[this] := 1; call P3.change end

process P3;
x,i:int
proc change begin i:=i+1; x:=x+1 end
begin i:=1; x:=0 end

```

The number of times procedure change in process P₃ has been called after termination of the program cannot be deduced in a proof about P₃. Not even after the private auxiliary variables j[1], j[2] and i are added. This is a result of the fact that requests are honoured implicitly; only the requesting process can keep pace with its number of requests.

A solution is obtained by a well known strategy: introduction of a global invariant of the program. This invariant will be denoted by GI. GI catches the interaction between the processes. In the example above $GI \equiv i=j[1]+j[2]+1$ will indeed provide the value of i on termination of the program. For reasons that will become clear in section 4.3 (rules R6 and R13), GI may contain only auxiliary variables (i.e. variables in AV). This is no limitation because auxiliary variables can trivially simulate the behaviour of process variables.

At present however, GI is not valid at all places, which contradicts the property of being a global invariant, e.g., in the example above the assignments to j [this] and i are not executed simultaneously. Therefore we introduce program sections S in which GI need not hold: bracketed sections. They will be denoted by < S >. Outside bracketed sections GI must be valid, and to ensure this assignments to variables free in GI are restricted to these sections.

Our first idea concerning the form of a bracketed section < S > within the program $[P_1 \parallel \dots \parallel P_n]$ was the following:

$$S \equiv S_1; \underline{\text{call}} P_j.\text{pr}(u_1, \dots, u_r, v_1, \dots, v_s); S_2 \quad (1 \leq j \leq n)$$

where pr denotes a procedure declared in P_j , and S_1 and S_2 do not contain guarded regions nor external requests; they usually contain assignments to auxiliary variables but may be empty. Because a procedure body may contain assignments to auxiliary variables of GI, all requests are cast within bracketed sections.

This definition is based on the fact that by the grain of interleaving that is used here, execution of a request is viewed as an elementary action within the requesting process. The strategy to be used depends on the validity of GI at the beginning of a bracketed section: GI is indeed a global invariant of the program if we can prove that GI is valid again at the end of each bracketed section within the program.

However, with this definition of bracketed section our strategy fails, which will become clear by the next program.

```

process P1;
i : int
begin i := 0; <i := 1; call P2.pr2> end

process P2;
j2 : int
proc pr2 begin <j2 := j2 + 1; call P3.pr3> end
begin j2 := 0 end

process P3;
x, k : int
proc pr3 begin <k := k + 1; x := x + 1> end
begin k := 0; x := 0 end

```

i , j_2 and k are auxiliary variables: i is used as counter of the requests to pr_2 in process P_1 , j_2 keeps pace with the number of the requests to pr_3 within the process P_3 , and k and j_2 are counters for the procedure body of pr_3 and pr_2 respectively. In order to prove $x = 1$ on termination of the program $[P_1 \parallel P_2 \parallel P_3]$, we need the global invariant $\text{GI} \equiv k = j_2 \wedge j_2 = i$.

Let us take a look at the bracketed section $\langle j_2 := j_2 + 1; \underline{\text{call}} P_3.\text{pr}_3 \rangle$

in process P_2 . This section will always occur within another bracketed section in the program, because every request is inside a bracketed section. In this case, the enclosing section is $\langle i := 1; \text{call } P_2.\text{pr2} \rangle$ in process P_1 . This situation is disastrous for our strategy: GI does not hold at the beginning of $\langle j2 := j2 + 1; \text{call } P_3.\text{pr3} \rangle$ because, by this time, $i = 1$ and $j = 0$.

Actually, the existence of nested bracketed sections is the cause of this. We eliminate this, by the introduction of countersections. The new definition is based on the fact that the validity of GI is in question only when parameter passing occurs, because the free variables in GI need be updated at these points. Inside the requesting process, there are at most two places at which parameter passing occurs (on account of call-by-value-result): at the beginning and at the end of the procedure execution.

Let pr be a procedure within P_j . Rearrange the assignments to auxiliary variables within the body so that pr has the following form:

$$\underline{\text{proc}} \text{pr}(x_1, \dots, x_r \# y_1, \dots, y_s) \underline{\text{begin}} T_1; T; T_2 \underline{\text{end}}$$

where T_1 and T_2 do not contain guarded regions nor external requests, and may be empty. Furthermore, in T no auxiliary variables free in GI appear outside bracketed sections. (The form need not be unique for pr !)

The next step is taken by associating the countersection T_1 with S_1 in the calling process and likewise for T_2 and S_2 . The boundaries T_1 and T_2 (remember the former bracketed section definition) are indicated by the brackets ">" and "<" respectively, and the begin, respectively, end-brackets of the procedure body:

$$\underline{\text{proc}} \text{pr}(x_1, \dots, x_r \# y_1, \dots, y_s) \underline{\text{begin}} T_1; > T <; T_2 \underline{\text{end}}$$

Note that $\langle S_1; T_1 \rangle$ and $\langle T_2; S_2 \rangle$ are not bracketed sections according to our previous definition of S , with $S \equiv S_1; \text{call } P_j.\text{pr}(u_1, \dots, u_r, v_1, \dots, v_s); S_2$, where:

Definition 3: A bracketed section within the DP program $[P_1 \parallel \dots \parallel P_n]$ is of the form $\langle S_1; T_1 \rangle$, $\langle T_2; S_2 \rangle$, or $\langle S_1; \text{call } P_j.\text{pr}(u_1, \dots, u_r, v_1, \dots, v_s); S_2 \rangle$, where:

- (i) $\langle S_1$, and $S_2 \rangle$ are taken from the construction $\langle S_1; \text{call } P_j.\text{pr}(u_1, \dots, u_r, v_1, \dots, v_s); S_2 \rangle$ in P_i ($1 \leq i \leq n$),
- (ii) $T_1 \rangle$, and $\langle T_2$ are taken from the construction

- proc $pr(x_1, \dots, x_r \# y_1, \dots, y_s)$ begin $T_1; > T <; T_2$ end, in process P_j ($1 \leq j \leq n$), and
- (iii) S_1, S_2, T_1 and T_2 do not contain guarded regions nor external requests, and may be empty. In the last situation they may be indicated with skip.

No assignments to auxiliary variables free in GI may appear outside bracketed sections.

In order to enforce correct use of GI and the PI's in the formulae, by means of which the axioms and rules of the proof system are expressed, we introduce the (proof-theoretical) environment, E. Besides these invariants, E also contains the texts of the procedure bodies in the program, needed for application of R12, and the variables declared in the processes within the program.

Definition 4: The (proof theoretical) environment E of the program $[P_1 \parallel \dots \parallel P_n]$ has the following composition:

$$E \equiv \langle\langle \text{private variables} \in P_i \rangle\rangle \\ | \langle pr_j(x_1, \dots, x_{in_j} \# y_1, \dots, y_{out_j}) \in P_i \rangle_{j=1}^{i_n} \rangle_{i=1}^n \\ | \langle PI_i \rangle_{i=1}^n | GI \rangle,$$

where:

- (1) the procedures in process P_i are numbered from 1 to i_n ,
- (2) no free variable of PI_j is subject to change in P_k , $k \neq j$,
- (3) no free variable of GI is subject to change outside a bracketed section.

Let each process in the program be numbered. We will fill the last "denotational holes" in the system by addition of the process index to the axioms and rules. In the notation, they appear directly after the statement in question, e.g., $\{p\} S, i \{q\}$. In this way, we ensure correct choice of the process invariant on application of the when-rule, R9, namely $PI_{\langle i \rangle}$, and on application of the call-rule, R12.

As will be evident from the when-rule, we also need a formal system for expressing and deducing properties of the location counter, such as Lamport's at (S), in (S), after (S), cf. [5]. We are still working on this.

4.2. List of axioms and proof rulesNotes

1. Where the index of the process has no particular role in the axiom or rule, it has been omitted, to improve readability.
2. $E\{p_1\} S_1 \{q_1\}, \dots, \{p_n\} S_n \{q_n\}$ is short for $E\{p_1\} S_1 \{q_1\}, \dots, E\{p_n\} S_n \{q_n\}$.

Axioms:

A1 assignment

$$E\{p[t/x]\} x := t \{p\}$$

A2 skip

$$E\{p\} \text{skip} \{p\}$$

A3 invariance

$$E\{p\} \text{call } P_j.\text{pr}(u_1, \dots, u_n, v_1, \dots, v_m), i\{p\}$$

provided $\text{freevar}(p) \cap \{v_1, \dots, v_m\} = \emptyset$, and no variables free in p are changed in process P_k , $k \neq i$.

Rules:R1 begin-end

$$\frac{E\{p\} S \{q\}}{E\{p\} \text{begin } S \text{ end } \{q\}}$$

where S denotes a statement.

R2 composition

$$\frac{E\{p\} S_1 \{q\}, \{q\} S_2 \{r\}}{E\{p\} S_1; S_2 \{r\}}$$

R3 consequence

$$\frac{p \rightarrow p_1, E\{p_1\} S \{q_1\}, q_1 \rightarrow q,}{E\{p\} S \{q\}}$$

$$\frac{p \rightarrow p_1, \{p_1\} [P_1 \parallel \dots \parallel P_n] \{q_1\}, q_1 \rightarrow q}{\{p\} [P_1 \parallel \dots \parallel P_n] \{q\}}$$

R4 conjunction

$$\frac{E\{p\} S \{q\}, \{p\} S \{r\}}{E\{p\} S \{q \wedge r\}}$$

R5 disjunction

$$\frac{E\{p\} S \{r\}, \{q\} S \{r\}}{E\{p \vee q\} S \{r\}}$$

R6 substitution

$$\frac{E \{p\} S \{q\}}{E \{p[z/x]\} S \{q\}}$$

provided $x \notin \text{freevar}(E, S, q)$

$$\frac{\{p\} [P_1 \parallel \dots \parallel P_n] \{q\} ,}{\{P[z/x]\} [P_1 \parallel \dots \parallel P_n] \{q\}}$$

provided $x \notin \text{freevar}(P_1, \dots, P_n, q)$ R7 if

$$\frac{E \{p \wedge b_1\} S_1 \{q\}, \dots, \{p \wedge b_n\} S_n \{q\}, p \rightarrow \prod_{i=1}^n b_i}{E \{p\} \text{if } b_1 : S_1 \mid \dots \mid b_n : S_n \text{ end } \{q\}}$$

R8 do

$$\frac{E \{p \wedge b_1\} S_1 \{p\}, \dots, \{p \wedge b_n\} S_n \{p\}}{E \{p\} \text{do } b_1 : S_1 \mid \dots \mid b_n : S_n \text{ end } \{p \wedge \prod_{i=1}^n b_i\}}$$

R9 when

$$\frac{E \{p \wedge b_1\} S_1, i \{q\}, \dots, \{p \wedge b_n\} S_n, i \{q\},}{p \wedge \prod_{j=1}^n b_j \rightarrow PI_i,} \\ \frac{\{b_1 \wedge PI_i \wedge \text{at}(S)\} S_1, i \{q\}, \dots, \{b_n \wedge PI_i \wedge \text{at}(S)\} S_n, i \{q\}}{E \{p\} \text{when } b_1 : S_1 \mid \dots \mid b_n : S_n \text{ end}, i \{q\}}$$

where S in $\text{at}(S)$ denotes the considered occurrence of the when-statement.R9' cycle

$$\text{cycle } b_1 : S_1 \mid \dots \mid b_n : S_n \text{ end} = \text{do } \text{true} : \text{when } b_1 : S_1 \mid \dots \mid b_n : S_n \text{ end } \text{end}$$

R10 local variables

$$\frac{E \{p\} S[z/x], i \{q\}}{E \{p\} \text{begin new } x; S \text{ end}, i \{q\}}$$

provided $z \notin \text{freevar}(E, S, p, q)$

R11 bracketed section

$$\frac{E \{p \wedge GI\} S \{q \wedge GI\}}{E \{p\} \langle S \rangle \{q\}}$$

R12 call

$$\frac{E \{p \wedge PI_j\} x_1[i] := u_1; \dots; x_n[i] := u_n; T_1, j \{p_1 \wedge p_2 \wedge GI\},}{E \{p_1\} T[x_1[i]/x_1, \dots, x_n[i]/x_n, y_1[i]/y_1, \dots, y_m[i]/y_m], j \{q_1\},} \\ \frac{E \{q_1 \wedge p_2 \wedge GI\} T_2; v_1 := y_1[i]; \dots; v_m := y_m[i], j \{q \wedge PI_j\}}{E \{p\} \text{call } P_j.\text{pr}(u_1, \dots, u_n, v_1, \dots, v_m), i \{q\}}$$

where (i) $x_k[i]$, respectively, $y_k[i]$ denote the fresh copies of x_k , respectively, y_k for the incarnation of procedure pr belonging to this request,

(ii) procedure pr in P_j is declared in E as follows:

proc $\text{pr}(x_1, \dots, x_n \# y_1, \dots, y_m)$ begin T_1 ; T <; T_2 end,

- (iii) free variables of p and q which do not occur in GI are not changed outside P_i ; $\{p_1\} T[\dots] \{q_1\}$ may not contain free variables subject to change in P_k , $k \neq j$; p_2 may not contain free variables subject to change in P_k , $k \neq i$.

R13 parallel composition

$$\frac{\langle E \rangle \{p_i\} \text{Init}_i, i \{PI_i\} \rangle_{i=1}^n}{\{ \bigwedge_{i=1}^n p_i \wedge GI \} [P_1 \parallel \dots \parallel P_n] \{ \bigwedge_{i=1}^n PI_i \wedge GI \}}$$

where (i) Init_i denotes the (annotated) initial statement of P_i ,
(ii) auxiliary variables and bracketed sections are contained in E and Init_i .

R14 AV

$$\frac{E \{p\} S' \{q\}}{E \{p\} S \{q\}}$$

provided $\text{free-variables}(q) \cap AV = \emptyset$, and S is obtained from S' by deletions of all assignments to variables of AV , as defined in definition 2 of section 4.1.

In addition axioms and rules formalizing location properties are needed (for $\text{at}(S)$, $\text{in}(S)$, $\text{after}(S)$, cf. [5]). These are not provided in the present version, but are needed in the formulation of R9, and the application of R12 (cf. the correctness proof of the monkey-banana example in section 5.3).

4.3. Justification of the proof system

Justification of E , PI , GI and the introduction of statement-index pairs in a formula is given in section 4.1 and will not be discussed here. The axioms A1 and A2 and the rules R2 to R5 are well-known and do not need explanation. Rule R1 is obvious. Rule R6 is of importance because of the elimination of auxiliary variables from pre-assertions. The axiom A3 will be explained directly after the discussion of rule R12.

(R7) According to the definition of the if-statement in section 2.6, the program is aborted when all guards evaluate to false on execution of this statement. In this case, $p \wedge \bigwedge_{i=1}^n b_i$ does not hold and the statement cannot be proved with the proposed rule, as should be. Besides this, R7 is the usual rule of alternative command.

(R8) R8 is the ordinary rule of the repetitive command.

(R9) Rule R9 is of great interest because the when-statement may act as a waiting point: when the guards of this statement evaluate to false, the process may resume an earlier operation or start a new one by honouring a request. In the discussion in section 4.1, we demand that at this moment PI_i holds, which is expressed by $p \wedge \bigwedge_{j=1}^n b_j \rightarrow PI_i$ in R9.

Whenever the statement is resumed again, anything might have happened to the variables in assertion p and, consequently, p need not hold. However, now one of the b_i 's is true and we just passed a waiting point, otherwise control in P_i could not have switched to this when-statement. Therefore, PI_i holds and we have arrived at a situation equivalent to the alternative command with precondition $PI_i \wedge at(S)$, where S denotes this particular occurrence of the when-statement, $at(S)$ expresses that flow of control reached S , and hence, $PI_i \wedge at(S)$ "specializes to that part of PI_i (using modus ponens) applicable to S " (cf. [5]). Thus, in order to prove that q holds after execution of the when-statement, at least

$$E] \{b_1 \wedge PI_i \wedge at(S)\} S_1 \{q\}, \dots, \{b_n \wedge PI_i \wedge at(S)\} S_n \{q\}$$

has to be proved (cf. R8).

In this case, validity of the first part of the premiss in R6:

$$E] \{p \wedge b_1\} S_1 \{q\}, \dots, \{p \wedge b_n\} S_n \{q\}$$

is easily checked, because $p \wedge b_j = \underline{\text{false}}$, for all j .

If the when-statement is not a waiting point, then at least one b_j is true on arrival. The statement is now equivalent to the corresponding alternative command and, consequently,

$$E] \{p \wedge b_1\} S_1 \{q\}, \dots, \{p \wedge b_n\} S_n \{q\} \text{ has to be proved.}$$

But in this case R9 forces also the proofs of $p \wedge \bigwedge_{j=1}^n b_j \rightarrow PI_i$, which is trivial because $p \wedge \bigwedge_{j=1}^n b_j = \underline{\text{false}}$, and of the third premiss:

$$E] \{b_1 \wedge PI_i \wedge at(S)\} S_1 \{q\}, \dots, \{b_n \wedge PI_i \wedge at(S)\} S_n \{q\}.$$

In practice, by appropriate manipulation of the auxiliary variables, i.e., by encoding that the disjunction of the guards of a when-statement did or did not hold, immediately prior to entry, one could obtain that $(p \wedge \bigvee_j b_j \rightarrow \neg(PI_i \wedge at(S)))$ holds in that context.

(R10) This is a familiar rule (cf. [3], ch. 6).

(R11) This rule should be clear from section 4.1.

(R12) R12, the call-rule is relatively simple in comparison with the usual procedure call-rules, because DP has no recursive procedures. The appearance of PI_j in the premiss has been explained in section 4.1, while discussing the arrival of control in P_j at a waiting point (P_j is ready to honour the request). The appearance of GI and its relation to the sections T_1, T_2 and the assertions p and q has been explained too in 4.1.

We have chosen this form for R12, because now the premiss $\{p_1\} T[\dots] \{q_1\}$ may be copied from the proof of the procedure body in P_j . Because each request is within a bracketed section, the application of R12 is always combined with the application of R11. This implies that the assertions p and q may contain, besides variables and auxiliary variables private to P_i , also auxiliary variables in GI. Observe that the actual input parameters need not be distinct from the actual output parameters.

Intermediate assertion p_2 is needed because q may contain free variables from P_i different from the output parameters v_1, \dots, v_m , and these are not allowed to occur freely in $\{p_1\} T[\dots] \{q_1\}$. The precise formulation of the conditions upon the free variables has to do with the wish to apply the technique of "freezing the variables" in correctness proofs, see ch. 6.

(A3) In the present version, A3 is not needed because of the appearance of p_2 in R12. Since A3 is needed in a projected extension of the formalism and its innocent use in the examples can be easily eliminated in favour of R12 in its present formulation, A3 is still included.

(R13) As a result of the termination convention (cf. 2.4) and the definition of bracketed sections (cf. 4.1) program-control is not within a bracketed section at the beginning nor at the end of execution of the program. By R11 and the restriction that no variables free in GI may be subject to change outside a bracketed section (cf. 4.1), GI is valid at the beginning and end of the program. Further, the consistent usage of E in the premisses of R13 implies validity of the conclusion, provided that these premisses were valid.

(R14) Variables in AV do not affect the flow of control during execution of the program and have no influence on the values of the variables within the processes (by definition, cf. 4.1). Consequently, when no variables of AV appear in the post-assertion, we may delete all assignments to variables of AV in the proved program and claim the same post-assertion for the new program with respect to the original pre-assertion.

5. PROOFS OF THE EXAMPLES IN SECTION 3

In order to make proofs readable, proof outlines will be given in which the processes P_1, \dots, P_n (modified by addition of auxiliary variables and bracketed sections to P'_1, \dots, P'_n) are annotated with assertions at appropriate places. Because environment E is clear from the text, expressing amongst other things that the PI's and GI are chosen once, we omit E in the proofs. The index, used in the axioms and proofrules to indicate the process in which the considered occurrence of a statement is situated, is likewise omitted. Where the rules and axioms, used for the proof of $\{p\} S \{q\}$, can be understood easily, we shall not mention them. Further, $\{p_1\}\{p_2\}$ occurring as adjacent assertions in a proof outline denote use of the consequence rule R3, with $p_1 \rightarrow p_2$. Insofar possible x_k , respectively, y_k is written instead of $x_k[i]$ and $y_k[i]$ in the use of rule R12.

5.1. Proof of example 3.1.

We must prove: $\{\text{true}\} [P_1 \parallel P_2] \{y = 0\}$

For this purpose, we choose the following invariants and proof outlines:

$PI_1 \equiv h_1 = x \wedge x = 0$,
 $PI_2 \equiv y = \text{last}(h_2)$,
 $GI \equiv h_1 = 0 \rightarrow h_1 = \text{last}(h_2)$,

```

process P'_1;
x, h_1 : int
{h_1 = -1} begin {h_1 = -1} x := 0; {h_1 = -1  $\wedge$  x = 0}
                <h_1 := 0; {PI_1} call P'_2.pass(x)>
                {PI_1}
                end {PI_1}

process P'_2;
y : int; h_2 : seq[2]int
proc pass(z : int) {PI_2} begin {PI_2} h_2 := h_2^<z>;>
                        {last(h_2) = z} y := z <{PI_2}
                        end {PI_2}
{h_2 = <1>} begin {h_2 = <1>} y := 1 {PI_2} end {PI_2}

```

(1) Verification of all assertions is straightforward (with A1, A2, R2, R3 and R4), except for those belonging to the bracketed section in process P'_1 .

To prove:

$$\{h_1 = -1 \wedge x = 0\} \langle h_1 := 0; \underline{\text{call}} P'_2.\text{pass}(x) \rangle \{PI_1\} \dots \quad (\text{i})$$

By rule R11, this reduces to proving

$$\{h_1 = -1 \wedge x = 0 \wedge GI\} h_1 := 0; \underline{\text{call}} P'_2.\text{pass}(x) \{PI_1 \wedge GI\},$$

and, hence, by A1 to

$$\{PI_1\} \underline{\text{call}} P'_2.\text{pass}(x) \{PI_1 \wedge GI\} \dots \quad (\text{ii})$$

We will prove this by the call rule (R12);

the premiss needed is:

$$\begin{aligned} &\{PI_1 \wedge PI_2\} z := x; h_2 := h_2 \hat{\langle z \rangle} \{z = 0 \wedge \text{last}(h_2) = z \wedge PI_1 \wedge GI\}, \\ &\{z = 0 \wedge \text{last}(h_2) = z\} y := z \{y = 0 \wedge PI_2\}, \\ &\{y = 0 \wedge PI_2 \wedge PI_1 \wedge GI\} \underline{\text{skip}} \{PI_1 \wedge GI \wedge PI_2\}, \end{aligned}$$

and follows by application of A1, A2, R2 and R3.

By R12, we now conclude (ii) and, hence, we proved (i).

(2) From the proof outlines we conclude

$$\begin{aligned} &\{h_1 = -1\} \text{Init}_1 \{PI_1\}, \text{ and} \\ &\{h_2 = \langle 1 \rangle\} \text{Init}_2 \{PI_2\}. \end{aligned}$$

Applying R13 and R3, we get

$$\{h_1 = -1 \wedge h_2 = \langle 1 \rangle \wedge GI\} [P'_1 \parallel P'_2] \{PI_1 \wedge PI_2 \wedge GI\} \{y = 0\}.$$

(3) Because $y \notin AV$, we may apply rule R14 to conclude

$$\{h_1 = -1 \wedge h_2 = \langle 1 \rangle \wedge GI\} [P_1 \parallel P_2] \{y = 0\}.$$

Substituting -1 for h_1 and $\langle 1 \rangle$ for h_2 , we finally derive by repeated application of R6:

$$\{\underline{\text{true}}\} [P_1 \parallel P_2] \{y = 0\}.$$

Q.E.D.

5.2. Proof of example 3.2.

We have to prove:

$$\{\underline{\text{true}}\} [\text{soldier} \parallel \text{military-department}[n]] \{\text{salary} = 1 \vee \dots \vee \text{salary} = n\}$$

We associate in the proof outlines with each process a number. The processes in the array are indicated with their array-index. Process soldier gets number 0.

The proof is along the same line as the previous example. We choose the following invariants and proof outlines:

$$PI_0 \equiv \text{salary} = \text{last}(h),$$

$$PI_i \equiv h_1[i] = 1, \text{ for } 1 \leq i \leq n,$$

$$GI \equiv \bigwedge_{i=1}^n (h_1[i] = 1 \rightarrow i \in h) \wedge \text{length}(h) = 1 + \sum_{i=1}^n h_1[i],$$

```

process military-department[n]';
h1[this] : int
{h1[this] = 0} begin {h1[this] = 0} < h1[this] := 1;
                    call soldier'.change(this) > {PIthis}
                    end {PIthis}

process soldier';
salary : int; h : seq[n+1]int
proc change(z:int) {PI0} begin {PI0} h := h^<z>;
                    {last(h) = z} salary := z < {PI0}
                    end {PI0}
{h = <1>} begin {h = <1>} salary := 1 {PI0} end {PI0}

```

(1) As in 5.1., we only verify the assertions belonging to bracketed sections, and this time only those occurring within process-array military-department[n]'. We prove:

$$\{h_1[\text{this}] = 0\} \langle h_1[\text{this}] := 1; \{PI_{\text{this}}\} \text{ call soldier'.change(this)} \rangle \{PI_{\text{this}}\}$$

Assume this = i_0 , then by R11 this reduces to proving:

$$\{h_1[i_0] = 0 \wedge GI\} h_1[i_0] := 1; \{PI_{i_0}\} \text{ call soldier'.change}(i_0) \{PI_{i_0} \wedge GI\}.$$

Hence, by A1 and the definitions of PI_{i_0} and GI, we have to prove:

$$\{PI_{i_0} \wedge \bigwedge_{\substack{i=1 \\ i \neq i_0}}^n \text{ and } (h_2[i] = 1 \rightarrow i \in h) \wedge \text{length}(h) = 1 + \sum_{\substack{i=1 \\ i \neq i_0}}^n \text{ and } h_1[i]\}$$

$$\text{ call soldier'.change}(i_0) \\ \{PI_{i_0} \wedge GI\}.$$

By R12, this follows from premiss

$$\{PI_{i_0} \wedge \bigwedge_{\substack{i=1 \\ i \neq i_0}}^n \text{ and } (h_1[i] = 1 \rightarrow i \in h) \wedge \text{length}(h) = 1 + \sum_{\substack{i=1 \\ i \neq i_0}}^n \text{ and } h_1[i] \wedge PI_0\}$$

$$z := i_0; h := h^{\langle z \rangle}$$

$$\begin{aligned}
& \{ \text{last}(h) = z \wedge \text{PI}_{i_0} \wedge \bigwedge_{i=1}^n (h_1[i] = 1 \rightarrow i \in h) \wedge \text{length}(h) = 1 + \sum_{i=1}^n h_1[i] \wedge z = i_0 \} \\
& \{ \text{last}(h) = z \wedge \text{PI}_{i_0} \wedge \text{GI} \}, \\
& \{ \text{last}(h) = z \} \text{salary} := z \{ \text{PI}_0 \}, \\
& \{ \text{PI}_0 \wedge \text{PI}_{i_0} \wedge \text{GI} \} \text{skip} \{ \text{PI}_{i_0} \wedge \text{GI} \wedge \text{PI}_0 \},
\end{aligned}$$

which formulae are all valid by the definitions and A1, A2 and R2.

(2) From the proof outlines, we conclude validity of

$$\begin{aligned}
& \{ h = \langle 1 \rangle \} \text{Init}_0 \{ \text{PI}_0 \}, \text{ and} \\
& \{ h_1[i] = 0 \} \text{Init}_i \{ \text{PI}_i \}, \quad 1 \leq i \leq n.
\end{aligned}$$

Next we apply rules R13 and R3, resulting in

$$\begin{aligned}
& \{ h = \langle 1 \rangle \wedge \bigwedge_{i=1}^n h_1[i] = 0 \wedge \text{GI} \} [P'_0 \parallel \dots \parallel P'_n] \\
& \{ \bigwedge_{i=0}^n \text{PI}_i \wedge \text{GI} \} \{ \text{salary} = 1 \vee \dots \vee \text{salary} = n \}.
\end{aligned}$$

(3) Analogous to part 3 of section 5.1., application of R14 and R6 to the formula above finishes the proof of

$$\begin{aligned}
& \{ \underline{\text{true}} \} [P_0 \parallel \dots \parallel P_n] \{ \text{salary} = 1 \vee \dots \vee \text{salary} = n \}, \text{ i.e., of} \\
& \{ \underline{\text{true}} \} [\text{soldier} \parallel \text{military_department}[n]] \{ \text{salary} = 1 \vee \dots \vee \text{salary} = n \} \\
& \text{(by the index convention at the beginning of this section)}.
\end{aligned}$$

Q.E.D.

5.3. Proof of example 3.3.

Let the programs be numbered as follows:

$$P_1 = \text{monkey}[1], \quad P_2 = \text{monkey}[2], \quad P_3 = \text{writer}.$$

To prove:

$$\{ \underline{\text{true}} \} [P_1 \parallel P_2 \parallel P_3] \{ \text{word} = \langle \text{HUMBUG} \rangle \vee \text{message} = \langle \text{no_bananas_today} \rangle \}.$$

For this purpose we choose the following invariants and proof outlines:

$$\begin{aligned}
\text{PI}_{\text{this}} & \equiv h[\text{this}] = 1, \quad \text{for this} = 1, 2, \\
\text{PI}_3 & \equiv (b \wedge d \rightarrow \text{HUM} \in \text{word}) \wedge (c \rightarrow \text{last}(\text{word}) = \text{BUG}) \wedge \\
& \quad \wedge \text{length}(\text{word}) = h_3 \wedge (\text{loc} = 1 \rightarrow \text{HUM}, \text{BUG} \in \text{word} \vee \\
& \quad \vee \text{message} = \langle \text{no_bananas_today} \rangle) \\
& \equiv \text{PI}'_3 \wedge \text{length}(\text{word}) = h_3, \\
\text{GI} & \equiv h_3 = h[1] + h[2].
\end{aligned}$$

```

process monkey[2]';
proc eat(x) {PIthis} begin
    {PIthis} "eat x" {PIthis}
    end {PIthis}
{h[this] = 0} begin
    {h[this] = 0} <h[this] := 1;
                                {PIthis} call writer'.type> {PIthis}
    end {PIthis}

process writer';
b, c, d : bool; word : seq[2] syllable; message : seq[16]char;
proc type
{PI3} begin {PI3} h3 := h3 + 1> {PI3' ∧ length(word) = h3 - 1}
    if true : {PI3' ∧ length(word) = h3 - 1}
        word := word^<HUM> {PI3 ∧ last(word) = HUM}
        b := true; c := false; d := true {PI3}
    | true : {PI3' ∧ length(word) = h3 - 1}
        word := word^<BUG> {PI3 ∧ last(word) = BUG}
        if c : {PI3 ∧ last(word) = BUG ∧ c}
            d := false; b := true {PI3}
        | ¬c : {PI3 ∧ last(word) = BUG ∧ ¬c}
            c := true {PI3}
        end {PI3}
    end {PI3}
<end {PI3}
{h3 = loc = 0} begin
    {h3 = loc = 0} b := c := d := false;
    message := word := <> {PI3 ∧ ¬b}
    when b : {b ∧ PI3}
        if c ∧ d : {b ∧ c ∧ d ∧ PI3} {HUM, BUG ∈ word ∧ PI3}
            loc := 1; {PI3 ∧ loc = 1}
            <call monkey[1]'.eat(banana)>;
            <call monkey[2]'.eat(banana)>
            {PI3 ∧ loc = 1}
        ¬(c ∧ d) : {PI3 ∧ ¬(c ∧ d)}
            message := <no_bananas_today>;
            {PI3 ∧ message = <no_bananas_today>}
            loc := 1 {PI3 ∧ loc = 1}
        end {PI3 ∧ loc = 1}
    end {PI3 ∧ loc = 1}
end {PI3 ∧ loc = 1}

```

1) The bracketed sections in P'_3 are verified easily. First we check the annotated bracketed sections in process array monkey', and then the annotated when-statement.

a) We have to prove:

$$\{h[\text{this}] = 0\} \langle h[\text{this}] := 1; \text{call writer'.type} \rangle \{PI_{\text{this}}\}.$$

By R11, this reduces to

$$\{h[\text{this}] = 0 \wedge GI\} h[\text{this}] := 1; \text{call writer'.type} \{PI_{\text{this}} \wedge GI\}.$$

Hence, by application of A1, we have to prove:

$$\{PI_{\text{this}} \wedge h_3 = h[1] + h[2] - 1\} \text{call writer'.type} \{PI_{\text{this}} \wedge GI\}.$$

which, by R12, reduces to proving (assume $\text{this} = i_0$):

$$\begin{aligned} \{PI_{i_0} \wedge h_3 = h[1] + h[2] - 1 \wedge PI_3\} h_3 := h_3 + 1 \\ \{PI_3 \wedge \text{length}(\text{word}) = h_3 - 1 \wedge PI_{i_0} \wedge GI\}, \\ \{PI_3 \wedge \text{length}(\text{word}) = h_3 - 1\} \text{if ... end} \{PI_3\} \\ \text{(cf. the proof outline of process } P'_3\text{),} \end{aligned}$$

$$\{PI_3 \wedge PI_{i_0} \wedge GI\} \text{skip} \{PI_3 \wedge PI_{i_0} \wedge GI\},$$

which all follow from the definitions, the proof outline of P_3 , A1 and A2.

b) to prove: $\{PI_3 \wedge \neg b\} \text{when } b : \text{if ... end end} \{PI_3 \wedge \text{loc} = 1\}$

For this when-statement in P'_3 ,

$$PI_3 \wedge \neg b \wedge b \rightarrow \text{false} \text{ holds,}$$

Which by A1, A3 and R7 validates

$$\{PI_3 \wedge \neg b \wedge b\} \text{if ... end} \{PI_3 \wedge \text{loc} = 1\}.$$

Furthermore, $PI_3 \wedge \neg b \wedge \neg b \rightarrow PI_3$.

The proof concerning the rest of the premiss for this application of R9 is outlined in the proof outline of P'_3 .

By R9, we conclude $\{PI_3 \wedge \neg b\} \text{when } b : \text{if ... end end} \{PI_3 \wedge \text{loc} = 1\}$.

2) Thus we proved

$$\begin{aligned} \{h[i] = 0\} \text{Init}_i \{PI_i\} \text{ for } i = 1, 2 \text{ and} \\ \{h_3 = \text{loc} = 0\} \text{Init}_3 \{PI_3 \wedge \text{loc} = 1\}. \end{aligned}$$

Application of R13, realizing that the location counter loc is used to encode after ($\text{Init}_{\text{writer}}$), that $\{p\} S \{\text{after}(S)\}$ is an axiom - cf. [5] - and application of R3 and R4 results in

$$\begin{aligned} \{h[1] = h[2] = h_3 = \text{loc} = 0 \wedge GI\} \{P'_1 \parallel P'_2 \parallel P'_3\} \{PI_1 \wedge PI_2 \wedge PI_3 \wedge GI \wedge \text{loc} = 1\} \\ \{\text{word} = \langle \text{HUMBUG} \rangle \vee \text{message} = \langle \text{no_bananas_today} \rangle\}. \end{aligned}$$

Now, analogous to part (3) in sections 5.1. and 5.2., successive application of R14 and R6 (with 0 substituted for $h[1]$, $h[2]$, h_3 and loc) finishes the proof of

$$\{\underline{\text{true}}\}[P_1 \parallel P_2 \parallel P_3]\{\text{word} = \langle \text{HUMBUG} \rangle \vee \text{message} = \langle \text{no_bananas_today} \rangle\}$$

Q.E.D.

5.4. Proof of example 3.4.

In order to prove

$$\{\text{value} = n \wedge 0 \leq n < m\}[\text{user} \parallel \text{calculate}[1] \parallel \dots \parallel \text{calculate}[m]]\{\text{value} = n!\}$$

we choose the following invariants and proof outlines:

$$PI_{\text{user def}} = PI_0 \equiv \text{value} = n!,$$

$$PI_{\text{calculate}[this]_{\text{def}}} = PI_{\text{this}} \equiv \underline{\text{true}}, \text{ for } \text{this} = 1, \dots, m,$$

$$GI \equiv \underline{\text{true}},$$

```

process user';
value : int
{value = n ∧ 0 ≤ n < m} begin
    {value = n ∧ 0 ≤ n < m} <call calculate[1]'.
                                faculty(value, value)> {PI0}
    end {PI0}

process calculate[m]';
proc faculty(x : int# y : int)
    {x = a ∧ 0 ≤ a ≤ m - this} begin>
        {x = a ∧ 0 ≤ a ≤ m - this} if x > 0 : {x = a ∧ 0 < a ≤ m - this}
                                <call calculate[succ]'.
                                    faculty(x-1, y)>
                                {y = (a-1)! ∧ x = a} y := y * x
                                {y = a!}
        | x = 0 : {x = a ∧ a = 0} y := 1 {y = a!}
        end {y = a!}
    <end {y = a!}
{true} begin skip end {true}

```

Next, the annotated bracketed sections within the processes will be proved; the other annotated statements are easily verified using A1, A2 and R7.

We want to prove:

$$\{value = n \wedge 0 \leq n < m\} \\ \langle \underline{\text{call}} \text{ calculate}[1]'.\text{faculty}(value, value) \rangle \\ \{value = n!\}$$

i.e. validity of the bracketed section in process user.

The request call calculate[1]'.faculty(value, value) leads to the request call calculate[2]'.faculty(value-1, y) in process calculate[1]', etc., until the request call calculate[n+1]'.faculty in process calculate[n]' ends the iteration. The processes requested in this iteration are defined, because $n+1 \leq m$.

a) Firstly, we have to prove this last request, i.e.,

$$\{x = 1 \wedge 1 \leq n+1 \leq m\} \langle \underline{\text{call}} \text{ calculate}[n+1]'.\text{faculty}(x-1, y) \rangle \{y = 0! \wedge x = 1\}.$$

By R11 and the definition of GI, this leads to proving

$$\{x = 1 \wedge 1 \leq n+1 \leq m\} \underline{\text{call}} \text{ calculate}[n+1]'.\text{faculty}(x-1, y) \{y = 0! \wedge x = 1\}.$$

For this, we must prove (R12)

$$\{x = 1 \wedge 1 \leq n+1 \leq m\} x[n] := x-1 \{x[n] = 0 \wedge x = 1\}, \\ \{x[n] = 0\} \\ \underline{\text{if}} x[n] > 0 : \{false\} \\ \quad \langle \underline{\text{call}} \text{ calculate}[n+1]'.\text{faculty}(x[n]-1, y[n]) \rangle \\ \quad \{false\} y[n] := y[n] * x[n] \{false\} \{y[n] = 0!\} \\ \quad | x[n] = 0 : \{x[n] = 0\} \\ \quad \quad y[n] := 1 \{y[n] = 0!\} \\ \underline{\text{end}} \{y[n] = 0!\} \\ \{y[n] = 0! \wedge x = 1\} y := y[n] \{y = 0! \wedge x = 1\},$$

which are valid by A1, A3, R11 and R7, the if-rule.

b) Secondly, assuming call calculate[n+1 - (j-1)]'.faculty(j-1, y) in calculate[n+1 - j]' to be correct for $1 \leq j < n$, we must prove as induction step call calculate[n+1 - j]'.faculty(j, y) in calculate[n-j]' to be correct.

Hence we have to prove:

$$\{x = j+1 \wedge 1 \leq j < n \wedge m > n\} \langle \underline{\text{call}} \text{ calculate}[n+1 - j]'.\text{faculty}(x-1, y) \rangle \\ \{y = j! \wedge x = j+1\}$$

We have: $\{x = j+1 \wedge 1 \leq j < n \wedge m > n\} x[n-j] := x-1$

$$\{x[n-j] = j \wedge 1 \leq j < n \wedge m > n \wedge x = j+1\},$$


```

{x[n-j] = j ∧ 1 ≤ j < n ∧ m > n}
  if x[n-j] > 0 : {x[n-j] = (j-1) + 1 ∧ 1 ≤ j < n ∧ m > n}
    <call calculate[(n+1) - (j-1)]'.faculty(x[n-j] - 1, y[n-j])>
      (this call is correct for 1 < (n+1) - (j-1) ≤ n+1 ≤ m)
      {x[n-j] = (j-1) + 1 ∧ y = (j-1)!}
      (by induction hypothesis)
      y[n-j] := y[n-j] * x[n-j]
      {y[n-j] = j!}
  | x[n-j] = 0 : {false} ....
    {false}{y[n-j] = j!}
end {y[n-j] = j!},
{y[n-j] = j! ∧ x = j+1} y := y[n-j]{y = j! ∧ x = j+1},

```

which by R12 yields the result.

c) Thirdly, from a) and b) follows by induction

```

{x = n ∧ 0 ≤ n < m}
  <call calculate[2]'.faculty(x-1, y)>{x = n ∧ y = (x-1)!}

```

in the proof outline of calculate[1]'.

By reasoning as above, we obtain

```

{value = n ∧ 0 ≤ n < m}
  <call calculate[1]'.faculty(value,value)>{value = n!}.

```

2) By the proof outlines, we deduced

```

{value = n ∧ 0 ≤ n < m} Init0 {PI0}, and
  {true} Initi {PIi}, for i = 1, ..., m.

```

Application of R13 yields

```

{value = n ∧ 0 ≤ n < m}[P'0 || ... || P'm]{value = n!},

```

by the definitions of PI_i and GI.

Finally, rule R14 delivers the result:

```

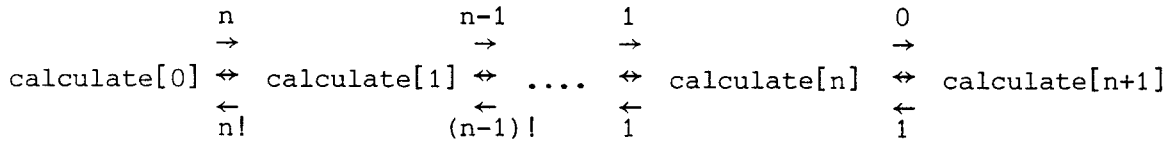
{value = n ∧ 0 ≤ n < m}[P0 || ... || Pm]{value = n!}, i.e.,
{value = n ∧ 0 ≤ n < m}[user || calculate[1] || ... || calculate[m]]{value = n!}

```

Q.E.D.

The proof given above reflects that in the DP processes concerned no synchronization takes place, which differs essentially from the kind encountered when considering procedure calls in sequential programming. Yet one might wish to make the DP-synchronization explicit. This can be done as follows:

Rename process user as calculate[0]. Then the functioning of the network can be visualized as below after call calculate[1].faculty(n, value) occurs:



Introduce auxiliary variables $a[j, j+1]$, $a[j+1, j]$ for $j=0, \dots, n$, by assumption initialized to 0. Replace <call calculate[j+1].faculty(n-1, y) > in calculate[j]' by <a[j, j+1] := n-j; call calculate[j+1].faculty(n-1, y)>, $j=0, \dots, n$, and add $a[j, j-1] := y$ as last instruction to the body of faculty in calculate[j]', $j=1, \dots, n+1$.

Now $GI \equiv \bigwedge_{j=0}^n (a[j+1, j] \neq 0 \rightarrow a[j+1, j] = a[j, j+1]!)$ expresses the functioning of the network.

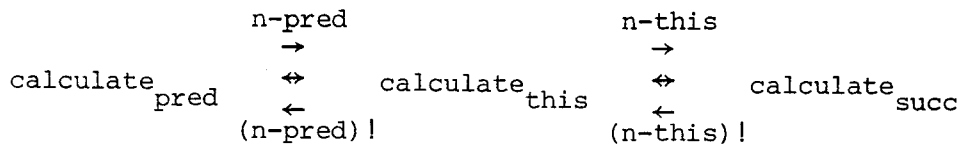
Also, one could introduce auxiliary variables receive[j], $j=0, \dots, n$ (by assumption initialized to 1), and replace

<a[j, j+1] := n-j; call calculate[j+1].faculty(n-1, y) > by
<a[j, j+1] := n-j; call calculate[j+1].faculty(n-1, y); receive[j] := y>.

Then $PI_j \equiv \text{receive}[j] = a[j, j+1]!$ expresses the I/O-behaviour of call calculate[j+1].faculty(n-j, y) in process calculate[j]'.

Obviously, superposing all this on top of the proof sketched above, amounts to adding a chain of trivialities.

Finally, in an alternative version of this program, using dynamic process creation for execution of faculty(x, y), the proof-theoretical environment of calculate_{this} would be based upon formalization of the functioning of the channels



by means of:

$$\begin{aligned}
 EI_{\text{this}} \equiv & (a_{\text{this,pred}} \neq 0 \rightarrow a_{\text{this,pred}} = (a_{\text{pred,this}})!) \wedge \\
 & (a_{\text{succ,this}} \neq 0 \rightarrow a_{\text{succ,this}} = (a_{\text{this,succ}})!),
 \end{aligned}$$

in analogy with the above.

Again we expect the correctness argument to amount to little more than superposition of a chain of trivialities on top of the correctness proof for the well-known sequential recursive procedure faculty(n, y), from which this exercise in distributed programming originates anyhow.

6. CORRECTNESS OF A DISTRIBUTED IMPLEMENTATION OF A PRIORITY QUEUE

In this section we give a proof for the following sorting program, which was first described by Brinch Hansen in [4].

```

process sort[n];
here : seq[2]int; rest, temp : int
proc put(c:int)when here.length < 2 : here.put(c) end
proc get( #v:int)when here.length= 1 : here.get(v) end
begin here := []; rest := 0;
  cycle
    here.length = 2:
      if here[1] ≤ here[2] : temp := here[2]; here := [here[1]]
      | here[1] > here[2] : temp := here[1]; here := [here[2]]
      end;
      call sort[succ].put(temp); rest := rest + 1
    | here.length= 0 ∧ rest > 0:
      call sort[succ].get(temp); rest := rest - 1;
      here := [temp]
  end

```

Brinch Hansen claims the program could be used as a priority scheduling queue as well, but when trying to prove it, we discovered an error. This is mended by substituting the guard `here.length < 2` in `proc put` by `here.length = 1 ∨ (here.length = 0 ∧ rest = 0)`.

In the following proof outline kept and away are auxiliary variables, denoting the bag of elements in the current process and of those sent to the following process, respectively.

For the union of bags we use the symbol \sqcup . The notation `con(here)`, in the assertions, stands for the bag of contents of `here`.

```

process sort[n]';
here : seq[2]int; rest, temp : int

proc put(c : int)
{PI ∧ GI} begin {PI ∧ GI} > {PI}
  when here.length= 1 ∨ (here.length= 0 ∧ rest = 0) :
    {PI ∧ (here.length= 1 ∨ (here.length= 0 ∧ rest = 0))}
    here.put(c) {post p}
  end; {post p}

```

```

    <{GI ∧ postp}
    kept := kept ∪ {c}    {PI ∧ outp}
  end {PI ∧ outp}
proc get(#v:int)
{PI ∧ GI} begin {PI ∧ GI} > {PI}
    when here.length = 1 : {PI ∧ here.length = 1}
        here.get(v) {postg}
    end; {postg}
    <{GI ∧ postg}
    kept := kept - {v}    {PI ∧ outg}
  end {PI ∧ outg}
begin {kept = away = ∅}
    here := []; rest := 0 {cyinv}
    cycle here.length = 2 : {PI ∧ here.length = 2}
        if here[1] ≤ here[2] : temp := here[2]; here := [here[1]]
        | here[1] > here[2] : temp := here[1]; here := [here[2]]
        end; {pre1}
        <call sort[succ]'.put(temp); rest := rest + 1;
        away := away ∪ {temp}; kept := kept - {temp}>
        {cyinv}
    | here.length = 0 ∧ rest > 0 : {PI ∧ here.length = 0 ∧ rest > 0}
        <call sort[succ]'.get(temp); rest := rest - 1;
        away := away - {temp}; kept := kept ∪ {temp}>; {post2}
        here := [temp]
        {cyinv}
    end
end
end

```

where $PI \equiv 0 \leq \text{here.length} \leq 2 \wedge \text{rest} \geq 0 \wedge \text{rest} = |\text{away}| \wedge \text{kept} = \text{con}(\text{here})$
 $\wedge (\text{here.length} > 0 \wedge \text{rest} > 0 \rightarrow \text{here}[1] \leq \min(\text{away}))$,

$GI \equiv \bigwedge_{i=1}^{n-1} \text{away}[i] = \text{away}[i+1] \cup \text{kept}[i+1]$,

$\text{postp} \equiv 0 < \text{here.length} \leq 2 \wedge \text{rest} \geq 0 \wedge \text{rest} = |\text{away}| \wedge \text{kept} \cup \{c\} = \text{con}(\text{here})$
 $\wedge (\text{rest} > 0 \rightarrow \text{here}[1] \leq \min(\text{away}))$,

$\text{outp} \equiv \text{away}[\text{pred}] \cup \{c\} = \text{away}[\text{this}] \cup \text{kept}[\text{this}]$
 $\wedge \bigwedge_{i=1}^{n-1} (i \neq \text{pred} \rightarrow \text{away}[i] = \text{away}[i+1] \cup \text{kept}[i+1])$,

$\text{postg} \equiv \text{here.length} = 0 \wedge \text{rest} \geq 0 \wedge \text{rest} = |\text{away}| \wedge \text{kept} = \{v\}$
 $\wedge (\text{rest} > 0 \rightarrow v \leq \min(\text{away}))$,

$$\begin{aligned} \text{outg} &\equiv \text{away}[\text{pred}] = \text{away}[\text{this}] \sqcup \{v\} \wedge \text{kept}[\text{this}] = \emptyset \\ &\quad \wedge (|\text{away}[\text{this}]| > 0 \rightarrow v \leq \min(\text{away}[\text{this}])) \\ &\quad \wedge \bigwedge_{i=1}^{n-1} (i \neq \text{pred} \rightarrow \text{away}[i] = \text{away}[i+1] \sqcup \text{kept}[i+1]), \\ \text{cyinv} &\equiv \text{PI} \wedge \neg (\text{here.length} = 2 \vee (\text{here.length} = 0 \wedge \text{rest} > 0)), \\ \text{pre1} &\equiv \text{here.length} = 1 \wedge \text{rest} \geq 0 \wedge \text{rest} = |\text{away}| \wedge \text{kept} = \text{con}(\text{here}) \sqcup \{\text{temp}\} \\ &\quad \wedge \text{here}[1] \leq \min(\text{away} \sqcup \{\text{temp}\}), \\ \text{post2} &\equiv \text{here.length} = 0 \wedge \text{rest} \geq 0 \wedge \text{rest} = |\text{away}| \wedge \text{kept} = \{\text{temp}\} \\ &\quad \wedge (\text{rest} > 0 \rightarrow \text{temp} \leq \min(\text{away})). \end{aligned}$$

We want to prove:

$$\{\text{aux} = \text{away}[0] = \text{away}[1] \sqcup \text{kept}[1]\} \text{call } \text{sort}[1]'.\text{get}(\text{elem}) \{\text{elem} = \min(\text{aux})\},$$

i.e., the element output by $\text{sort}[1]'.\text{get}$ is the least of all elements still in the sorting process. To prove this we shall have to prove the correctness of the assertions in $\text{sort}[1]'$, and therefore in $\text{sort}[2]'$, and so on.

We prove by induction the correctness of the assertions in $\text{sort}[i]'$ for i from n down to 1, assuming $\text{sort}[n+1]'$ will not be called (this is the case iff $\text{rest}[1] \leq n-1$, which we shall not prove now). The assumption tells us $\text{sort}[n]'$ will never go into the cycle. So, the correctness of the initialization part of $\text{sort}[n]'$ is trivial.

To prove the correctness of the procedures we have to know the meaning of here.put and here.get . Obviously here.put stands for $\text{here} := \text{here}[c]$ and here.get for $v := \text{here}[1]$; $\text{here} := []$. Now $\{p\} \text{here.put}(c) \{q\}$ and $\{p\} \text{here.get}(v) \{q\}$, with the p and q defined above, follow by A1, R2 and R3. Note the importance of $\text{here}[1] \leq \min(\text{away})$ in PI in the deduction of outg .

Because in both cases the precondition is PI, the first and last premisses of the when-rule are equivalent and we just proved them; the second premiss is a mere triviality, so by the when-rule we prove the part between $>$ and $<$ of the procedures. The parts before $>$ and after $<$ are easy.

Note that in this proof we did not use the induction hypothesis, so the proof holds for all processes.

Now we shall prove the correctness of the proof outlines for the initial statements of $\text{sort}[i]'$ for $i < n$, assuming we have a complete proof for $\text{sort}[i+1]'$. By the usual rules the part before the cycle is correct.

To prove the cycle, we have to prove, by R9':

$$\begin{aligned} &\{\text{PI} \wedge \text{here.length} = 2\} \text{if } \dots > \{\text{cyinv}\}, \\ &\{\text{PI} \wedge \text{here.length} = 0 \wedge \text{rest} > 0\} \text{call } \dots[\text{temp}] \{\text{cyinv}\}, \end{aligned}$$

$$\{ \text{cyinv} \wedge \text{here.length} = 2 \} \text{ if } \dots \{ \text{cyinv} \},$$

$$\{ \text{cyinv} \wedge \text{here.length} = 0 \wedge \text{rest} > 0 \} \text{ <call } \dots [\text{temp}] \{ \text{cyinv} \},$$

$$\text{cyinv} \wedge \neg (\text{here.length} = 2 \vee (\text{here.length} = 0 \wedge \text{rest} > 0)) \rightarrow \text{PI}.$$

Because $\text{cyinv} \rightarrow \text{PI}$ it is sufficient to prove the first two clauses. The difficult parts are the bracketed sections for which we give more elaborated proof outlines (the other parts are left to the reader):

a) $\{ \text{pre1} \}$
 $\langle \{ \text{pre1} \wedge \text{GI} \} \text{ call } \text{sort}[\text{succ}]'.\text{put}(\text{temp}) \{ \text{out1} \}$
 $\text{rest} := \text{rest} + 1; \text{away} := \text{away} \sqcup \{ \text{temp} \}; \text{kept} := \text{kept} - \{ \text{temp} \}$
 $\{ \text{post1} \wedge \text{GI} \} \rangle \{ \text{post1} \},$
 with $\text{post1} \equiv \text{cyinv},$
 pre1 is defined as above,
 $\text{out1} \equiv \text{here.length} = 1 \wedge \text{rest} \geq 0 \wedge \text{rest} = |\text{away}| \wedge \text{kept} = \text{con}(\text{here}) \sqcup \{ \text{temp} \} \wedge$
 $\wedge \text{here}[1] \leq \min(\text{away} \sqcup \{ \text{temp} \}) \wedge \text{away}[\text{this}] \sqcup \{ \text{temp} \} = \text{away}[\text{succ}] \sqcup$
 $\sqcup \text{kept}[\text{succ}] \wedge \bigwedge_{i=1}^{n-1} (i \neq \text{this} \rightarrow \text{away}[i] = \text{away}[i+1] \sqcup \text{kept}[i+1]).$

To prove $\{ \text{pre1} \wedge \text{GI} \} \text{ call } \text{sort}[\text{succ}]'.\text{put}(\text{temp}) \{ \text{out1} \}.$

We must prove (R12):

$$\{ \text{pre1} \wedge \text{GI} \wedge \text{PI}_{\text{succ}} \} \text{ c}[\text{this}] := \text{temp} \{ \text{GI} \wedge \text{PI}_{\text{succ}} \wedge \text{pre1} \},$$

$$\{ \text{PI}_{\text{succ}} \} \text{ Body.put} \{ \text{postp}_{\text{succ}} \} \text{ and} \tag{i}$$

$$\{ \text{postp}_{\text{succ}} \wedge \text{pre1} \wedge \text{GI} \} \text{ kept}[\text{succ}] := \text{kept}[\text{succ}] \sqcup \{ \text{c}[\text{this}] \} \{ \text{out1} \wedge \text{PI}_{\text{succ}} \}.$$

But this is not strong enough, so we add a new auxiliary variable *freeze* to hold the value of *temp*. The clauses now become:

$$\{ \text{pre1} \wedge \text{GI} \wedge \text{PI}_{\text{succ}} \wedge \text{freeze} = \text{temp} \} \text{ c}[\text{this}] := \text{temp}$$

$$\{ \text{GI} \wedge \text{PI}_{\text{succ}} \wedge \text{c}[\text{this}] = \text{freeze} \wedge \text{pre1} \wedge \text{freeze} = \text{temp} \},$$

$$\{ \text{PI}_{\text{succ}} \wedge \text{c}[\text{this}] = \text{freeze} \} \text{ Body.put} \{ \text{postput}_{\text{succ}} \wedge \text{c}[\text{this}] = \text{freeze} \},$$

$$\{ \text{postput}_{\text{succ}} \wedge \text{c}[\text{this}] = \text{freeze} \wedge \text{pre1} \wedge \text{freeze} = \text{temp} \wedge \text{GI} \}$$

$$\text{kept}[\text{succ}] := \text{kept}[\text{succ}] \sqcup \{ \text{c}[\text{this}] \} \{ \text{out1} \wedge \text{PI}_{\text{succ}} \}.$$

The first and last clause hold by A1 and R3; the second clause without *freeze* (as in (i)) was correct by our induction hypothesis. But now a problem occurs for which we need the addition of *at(S)* in the when-rule: if we want to deduce $\text{c}[\text{this}] = \text{freeze}$ we have to know $\text{PI}_{\text{succ}} \wedge b \rightarrow \text{c}[\text{this}] = \text{freeze}$, but that would be impossible without the use of *at(S)*. We now use a new PI which is $\text{PI} \wedge (\text{at}(S) \rightarrow \text{c}[\text{pred}] = \text{freeze})$ where *S* is the when-statement in proc *put*. By choosing

$$p1 \equiv \text{PI}_{\text{succ}} \wedge \text{c}[\text{this}] = \text{freeze},$$

$$p2 \equiv \text{pre1} \wedge \text{freeze} = \text{temp}, \text{ and}$$

$$p3 \equiv \text{postp}_{\text{succ}} \wedge \text{c}[\text{this}] = \text{freeze},$$

in the application of the when-rule we obtain:

$$\{pre1 \wedge GI \wedge freeze = temp\} \text{ call sort}[succ]'.put(temp) \{out1\}.$$

By substituting temp for freeze we get the statement to be proved (R6 and R3).

The remainder of this proof outline follows by application of assignment axiom (A1), consequencerule (R3), compositionrule (R2) and finally bracketed section rule (R11).

b) The other proof outline is:

$$\begin{aligned} &\{pre2\} \\ &<\{pre2 \wedge GI\} \text{ call sort}[succ]'.get(temp) \{out2\} \\ &rest := rest - 1; away := away - \{temp\}; kept := kept \sqcup \{temp\} \\ &\{post2 \wedge GI\} > \{post2\}, \end{aligned}$$

where post2 is defined above,

$$\begin{aligned} pre2 &\equiv PI \wedge here.length = 0 \wedge rest > 0, \\ out2 &\equiv here.length = 0 \wedge rest > 0 \wedge rest = |away| \wedge kept = con(here) \wedge \\ &\wedge temp \leq \min(away) \wedge away[this] = away[succ] \sqcup \{temp\} \wedge \\ &\wedge kept[succ] = \emptyset \wedge \bigwedge_{i=1}^{n-1} (i \neq this \rightarrow away[i] = kept[i+1] \sqcup away[i+1]). \end{aligned}$$

To prove $\{pre2 \wedge GI\} \text{ call sort}[succ]'.get(temp) \{out2\}$, we have to prove, by R12:

$$\begin{aligned} &\{pre2 \wedge GI \wedge PI_{succ}\} \text{ skip } \{pre2 \wedge GI \wedge PI_{succ}\}, \\ &\{PI_{succ}\} \text{ Body.get } \{postg_{succ}\}, \\ &\{postg_{succ} \wedge pre2 \wedge GI\} kept[succ] := kept[succ] - \{v[this]\}; \\ &\quad temp := v[this] \{out2 \wedge PI_{succ}\}. \end{aligned}$$

The first clause is trivial; the second one was proved (induction hypothesis) in the proof of $\text{sort}[succ]'$, and the third one follows by A1, R2 and R3. By these same rules one can prove the remainder of the bracketed section. Application of the bracketed sectionrule (R11) yields what we wanted to prove.

Now we have proved the premiss, belonging to this application of R9', and, thereby, we proved the initial statements of the sorting processes correct.

The proof of

$$\{aux = away[0] = away[1] \sqcup kept[1]\} \text{ call sort}[1].get(elem) \{elem = \min(aux)\}$$

is analogous to the proof of the second bracketed section, and justifies the heading of this paragraph.

Q.E.D.

7. CONCLUSIONS

We have developed a deductive system for proving properties of DP programs.

Unfortunately the system up to now is not complete w.r.t. proofs of properties about programs not free from deadlock w.r.t. external requests. This will be shown by the following program:

```

process P1;
x : int
proc pr1 begin skip end
begin x := 0; call P2.pr2(x) end

process P2;
proc pr2(#y : int) if true : y := 37; call P1.pr1
    | true : y := 73
    end
begin skip end

```

The program will deadlock, when the first alternative of the if-statement within the procedure pr2 is chosen on execution of P₁'s request (cf. 2.5). Consequently, after termination of the program $x = 73$ holds. However we are not able to prove exactly this post-assertion, because the assertion depends on information about (durable) suspension. In the present system, we can prove $x = 37 \vee x = 73$. The proof is easy and therefore omitted. We are still working on this problem and, at present, we are indeed able to prove the program above w.r.t. the post-condition $x = 73$. But more study is needed for the analogous case with guarded regions.

Besides, this phenomenon raises exciting possibilities for the implementation of recursion within a fixed network of distributed processes.

Namely, by allowing inter-process communication in the requesting process, during its suspension. Also, in this direction, research is going on, in order to find the appropriate version of Scott's induction rule.

Another complication concerns the index mechanism of an array of processes. With the proof system, so far, we cannot handle cases in which the existence of requested processes in the array, i.e., their index being between the array bounds, is of importance to the proof.

The "germ" of our proof system is the separation of proofs of the various processes. The proof of a single process, i.e., program module in the terminology of [7], depends on assertions that cannot be modified by the concurrent actions of other processes. To this end, private auxiliary variables (to store the history of the process, as far as concerned), process invariants, a global invariant, and bracketed sections (to indicate elementary, i.e., atomic actions) are introduced.

Faith in this method was gained, in particular, through Owicki's paper [7]. In this paper, Owicki presents an analogous (w.r.t. auxiliary variables) method for proving shared data types, which are similar to the common procedures in DP. For each program module, so called, "private" variables are included, to store its sharing history w.r.t. a particular data type. To each data type a , so called, "global" variable is added, to store its sharing history w.r.t. the program modules. In this way, "private" and "global" variables can be charged only when sharing of this particular data type takes place. Thus, our auxiliary variables (AV) are similar to Owicki's "global" and "private" variables.

Our particular set-up is motivated by our wish to prove at a later stage relative completeness of our proof system, using techniques from K.R. Apt's manuscript of a completeness proof for the CSP-proof system of [2].

In this paper global program control is reproduced with the help of auxiliary variables and the global invariant, GI, while local process control is modelled by the use of variables and auxiliary variables private to the process and by the process invariant PI. While GI registers the communications between the processes, PI functions as go-between for the variables of the process and the variables in GI. In section 4.1, we showed that PI alone was not sufficient, whereas certainly GI cannot take the role of PI in itself, because we want to keep the proofs of the processes private w.r.t. the variables used in them; besides, their extensions do not coincide (e.g. PI need not hold at the beginning of a process). The arguments leading to the present system have all been motivated within the article.

Finally we want to thank Krzysztof Apt for the opportunity to present and discuss this proof system in its various stages, and, especially, Nissim Francez for the discussion of this material during his visit at the University of Utrecht in August 1980, during which the outline of the system came in sight. We are grateful to various members of the Department of Computer Science of the Mathematical Centre in Amsterdam for the opportunity to present and discuss an earlier stage of the system.

REFERENCES

- [1] Apt, K.R., Recursive assertions and parallel programs,
to be published in Acta Informatica.
- [2] Apt, K.R., N. Francez and W.P. de Roever, A Proof System for
Communicating Sequential Processes. ACM Trans. Program.
Lang.-syst. 2, 3 (July 1980), 359-385.
- [3] Bakker, J.W. de, Mathematical Theory of Program Correctness, Series
in Computer Science, Prentice-Hall International, 1980.
- [4] Brinch Hansen, P., Distributed Processes: A Concurrent Programming
Concept., Comm. ACM 21, 11 (November 1978), 934-941.
- [5] Lamport, L., The Hoare's Logic of Concurrent Programs. Acta Inf.
14 (1980), 21-37.
- [6] Levin, G.M., A Proof Technique for Communicating Sequential Processes
(with an example), Techn. Rep., Computer Science Dep., Cornell
University, 1979.
- [7] Owicki, S., Specifications and proofs for abstract data types in
concurrent programs, Rep. TR 133, CRC Stanford University, 1977.
- [8] Owicki, S. and D. Gries, Verifying properties of parallel programs:
An Axiomatic Approach, Comm. ACM 19, 5 (May 1976) 279-285.
- [9] Owicki, S. and D. Gries, An axiomatic Proof Technique for Parallel
Programs I. Acta Inf. 6 (1976) 319-340.
- [10] Welsh, J., A.M. Lister and E.J. Salzman, A comparison of two notations
for Process Communication. Department of Computer Science, University
of Queensland, St. Lucia, Qld 4067.